Technical Specifications for VDS6000 Series PC Oscilloscope

Main Part

Bandwidth	VDS6102	100 MHz		
	VDS6152	150 MHz		
	VDS6102A	8-bit mode	100 MHz	
		12-bit mode	100 MHz	
		14-bit mode	20 MHz	
	VDS6152A	8-bit mode	150 MHz	
		12-bit mode	100 MHz	
		14-bit mode	20 MHz	
Vertical Resolution (A/D)	VDS6102 VDS6152	8 bits		
	VDS6102A VDS6152A	8 bits/12 bits/14 bits		
Sampling Rate	VDS6102 VDS6152	2-CH working	500 MS/s	
		1-CH working	1 GS/s	
	VDS6102A VDS6152A	2-CH working	8-bit mode	500 MS/s
			12-bit mode	250 MS/s
			14-bit mode	100 MS/s
		1-CH working	8-bit mode	1 GS/s
			12-bit mode	500 MS/s
			14-bit mode	100 MS/s
Rise Time	VDS6102 VDS6102A	≤ 3.5 ns		
	VDS6152 VDS6152A	≤ 2.3 ns		
Horizontal Scale (s/div)	5ns/div - 100s/div, step by 1 - 2 - 5			
Sampling Mode	sample, peak detect, average			
Channel Q'nty	3 (2 main one + 1 auxilary)			
Record Length	max 10M			
Input Coupling	DC, AC, ground			
Input Impedance	1 MΩ ± 2%, in parallel with 15 pF ± 5 pF			
Max Input Voltage	40 V (DC + AC Peak)			
DC Gain Accuracy	VDS6102 VDS6152	3% when ≥ 2 mV		
	VDS6102A VDS6152A	2% when ≥ 2 mV		
Vertical Sensitivity	2 mV/div - 5 V/div			
Trigger Type	edge, video, slope, pulse			

Trigger Mode	auto, normal, single
Line/Field Frequency (video)	supported standards: NTSC, PAL and SECAM broadcast systems
Automatic Measurement	Vpp, Vmax, Vmin, Vtop, Vbase, Vamp, Vavg, Vrms, Overshoot, Preshoot, Freq, Period, Rise Time, Fall Time, Delay A→B 卐, Delay A→B 卍, +Width, -Width, +Duty, -Duty

Function Generator Part

Standard Waveforms	sine, square, ramp, and pulse
Frequency Output	5 MHz
Sampling Rate	25 MSa/s
Channel Q'nty	1
Vertical Resolution	10 bits
Amplitude Range	10 mVpp – 5 Vpp
DC Offset Range (AC+DC)	± 2.5V
Output Impedance	50 Ω (typical)

General Part

Communication Interface	USB device (type-C), USB host (Wi-Fi extension supported), LAN
Wi-Fi Module	available in option
Power Source	5 - 15 VDC / 1.2A
Power Consumption	≤ 8 W
Device Dimension	w/h/d 190 x 120 x 18 mm
Weight	0.38 kg

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